Proposed changes to 9U Motherboard Design

1. Move Buffer Control logic from motherboard to PMC
2. Shorten Logic PMC by 20mm (249x299 -> 249x279mm)

Advantages:
- Decouples motherboard and Buffer Control designs
- Permits faster prototyping of motherboard

Disadvantages:
- Reduces available logic space by 7-25% (depending on original size)
- Changes footprint of logic PMC slightly
  (SCL connectors must move)

Additional simplifications proposed for first motherboard:
- Leave out on-board JTAG control (just provide connectors)
- Simplified power distribution (ignore FNAL standards)
Proposed changes to motherboard design:

1. Shrink logic board from 249x299 to 249x279
2. Move Buffer Controller to PMC

Advantages:

- Decouples Buffer Control from Motherboard
- Allows simpler, faster motherboard prototype