Review of L2STT  
E. Hazen - 25 Feb 2000  

**Generic 9U VME Motherboard**

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**Current Status**

- All I/Os pinouts specified
- Most logic functionality is defined by standards (VME, PCI, JTAG) (except L3 buffer control...)
- All Connector Placement Defined

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**Schedule**

- Detailed Engineering Design to begin 4/1
- Prototypes Available by 8/1/00