HCAL DCC Demonstrator

3-Channel LVDS Serial Link Receiver
(6 used in final DCC)
_PC-MIP PCI standard_

DCC Logic Board
-TTCTx
-Xilinx VirtexII
-XC2V1000
-2Mx32 DDRSDRAM
-S-Link Source Card
(optical fibre to DAQ)

Companion LVDS Serial Link Transmitter
(for testing only)
HCAL DCC Status

✔ VME Motherboard
  – Two prototypes working; 5 more boards being built

✔ Link Receiver Board (LRB)
  – 10 second-generation boards working

• DCC Logic Board
  ✔ PCB Design complete; being fabricated
  • FPGA coding underway

✔ Test Stand Hardware Complete
  – Pentium/Linux computer, TTC System Working
  – Link Transmitters to simulate HRC working
  – S-Link cards available