HF Readout Chain Update

- New QIE boards done (?) stuffed - end Jan (?)
- Skew-clear cables due next week
- Patch panel prototypes due today
  (2\textsuperscript{nd} revision already needed  <sigh>)

- Also... update on DCC design
HF QIE Digitizer Board
Blank PCBs at assembly house (??)

36 pin MDR Connector
(12 pairs, each with GND)

Optical Outputs use
Honeywell ST connector VCSEL
PMT Box Cabling

MMCX on prototypes
Soldered for production?

SMB plug/socket
for patch panel

Patch Panel
prototypes due
Friday

Final layout will
be dual-connector
version (more compact)

50 Ω mini-coax

Patch Panel SMD V1.0 by Emanuel Machado (BU/EDF 2003)
DCC Pre-Production Design

S-Link 64 at front panel

Jumper PCB for TTC, sTTS (RJ-45's on front panel)

DCC updates for Pre-Production:
- mechanical changes shown
  (mechanical prototype due next week)
- Firmware changes:
  o Data format per RUWG
  o Synchronization checking
  o Error / Overflow reporting