Event-Building from up to 9 HTRs

Automatic event building across PCI bus by receiver logic
Event number checking
Controlled by TTC L1A
Data rate ~ 100mbyte/sec (limited by PCI)

Event Filtering to several output streams

Pre-scaling for monitoring/spying
Selection based on header bits

Error Checking and Monitoring

Event number check against TTC L1A number
Link error monitoring (links have built-in Hamming ECC)
Time-out after L1A
FIFO occupancy monitoring