Current Status (2/1/01)

VME Motherboard
2 prototypes tested and working
Components for 10 (5 for CMS) on order

Link Receiver Board
2 prototypes (2nd revision) under test
Components/PCBs for 10 in stock

DCC Logic Module
CAD Schematic Done (first draft)
PCB Layout to start in 2–3 weeks (after D0 prototype layout)
Conceptual design for FPGA coding underway

*BU Political Problems Solved –
Rohlf is 100% on the project
Wu is DCC project engineer (and available full–time now)*