	LTB PCB Revision 2.1	**06/11/2001**																				
	Comment	**Manufacturer, Vendor**	**Footprint**	**Quantity**	**Designators**																	
1	0.33uF, 16v, X7R, PCC1817CT-ND	Digi-Key	0805	18	C24	C13	C2	C4	C3	C5	C8	C18	C20	C7	C6	C10	C11	C12	C14	C16	C17	C19
2	10k		0805	9	R8	R1	R9	R3	R7	R6	R5	R4	R2									
3	10nF		0805	1	C9																	
4	6.8uF, 6.3v, X5R, PCC1938CT-ND	Digi-Key	1206	3	C22	C1	C15															
5	**AT17LV512A-10JC (*)**	Atmel	PLCC-20	1	U5																	
6	**EPC2TC32 (*)**	Altera	TQFP-32	1	U8																	
7	**ATMEL-CONFIG (10 pins connector) (**)**		LVDS-NEW	1	J8																	
8	**JTAG-NEW (10 pins connector) (**)**		LVDS-NEW	1	J6																	
9	DS90CR285MTD	National	SOP-56	3	U1	U2	U3															
10	EP1K30QC208-2	Altera	PQFP-208	1	U4																	
11	LP2989IM-2.5	National	SO-8	1	U9																	
12	LVDS-CON (10 pins connector)		LVDS-CON	3	J3	J2	J1															
13	PCMIP-J1 (PC MIP connector)		PCMIP-J	1	J4																	
14	PCMIP-J2 (PC MIP connector)		PCMIP-J	1	J5																	
15	PCMIP-J3 (PC MIP connector)		PCMIP-J	1	J7																	
16	W152-2G	Cypress	SOIC-16	1	U6																	
	***) Position #5 and #6 should be purchased and mounted ether one or another**																					
	****) Same note for positions #7 and #8**																					