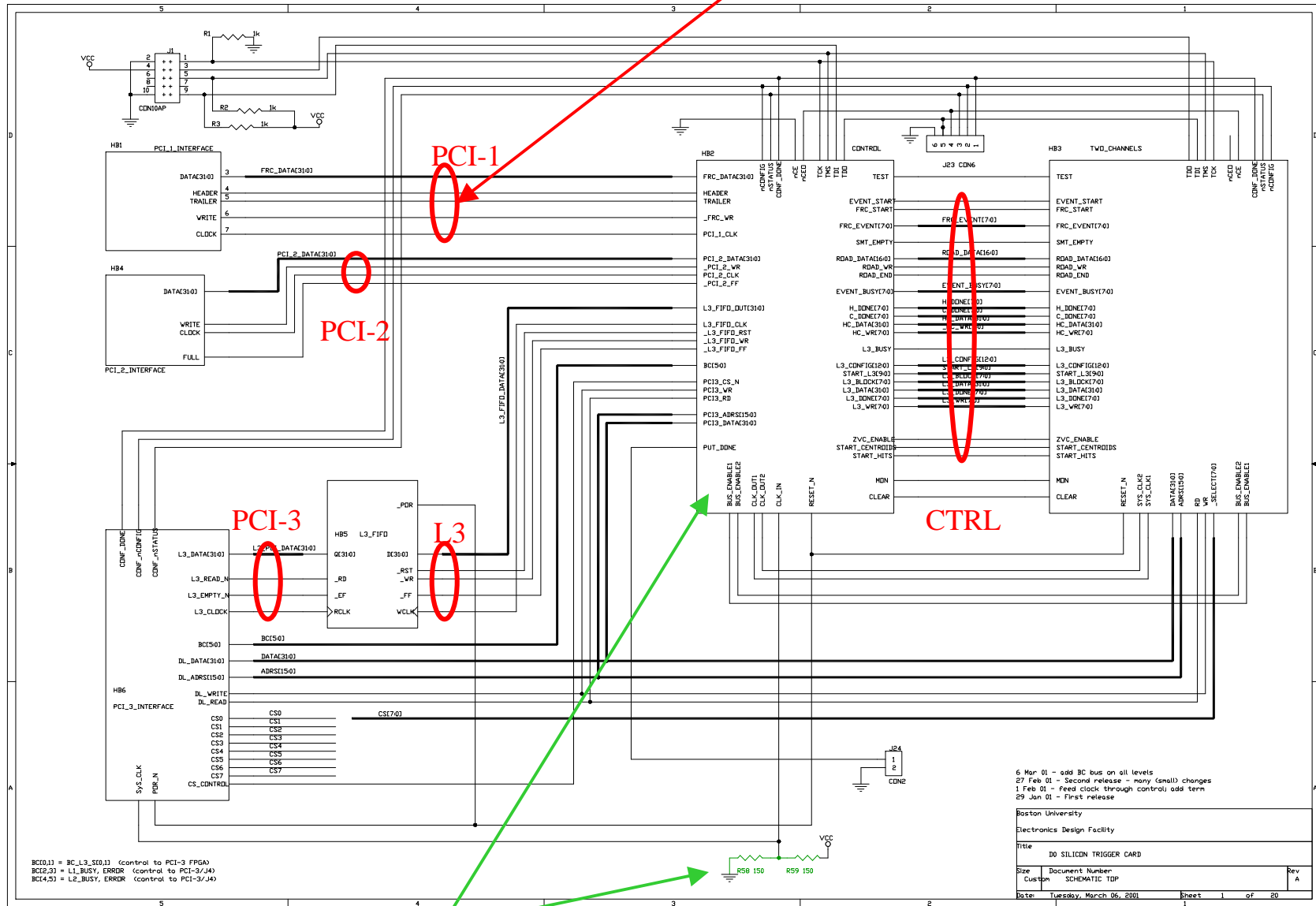


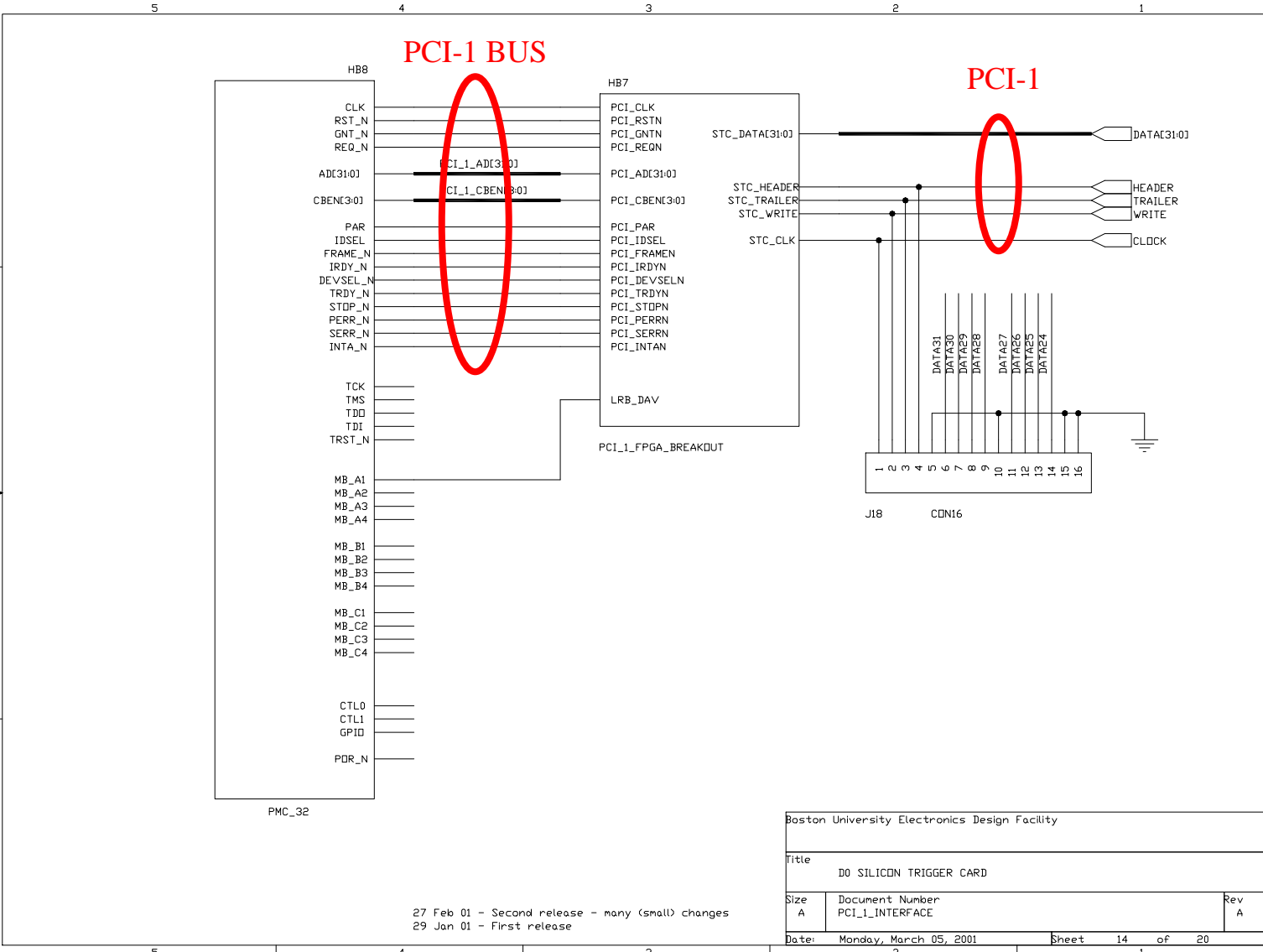
# Top Sheet

Red groups should be kept equal length (within group) to ~ 2cm



Terminator Near IC

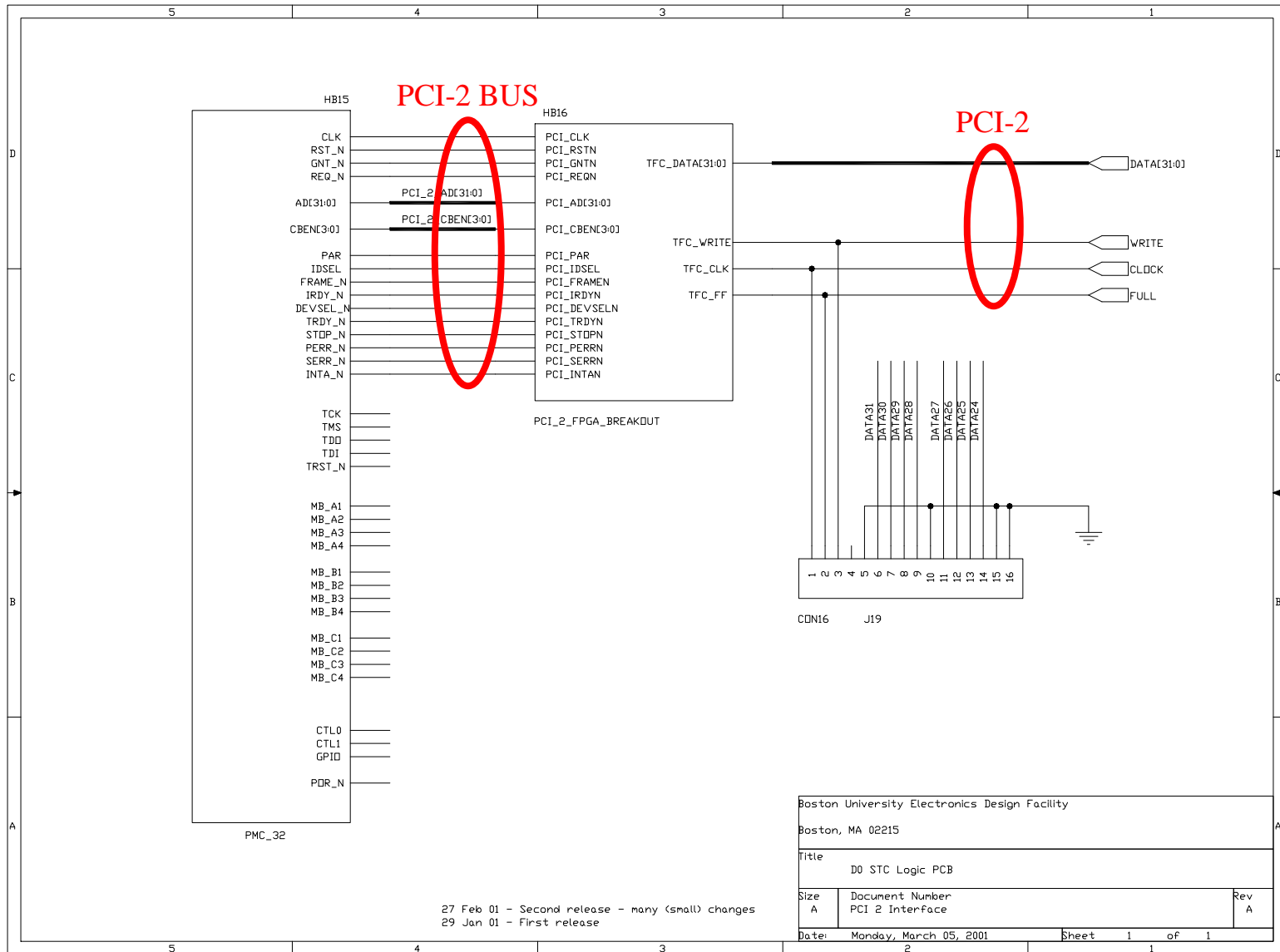
# PCI Bus 1



27 Feb 01 - Second release - many (small) changes  
 29 Jan 01 - First release

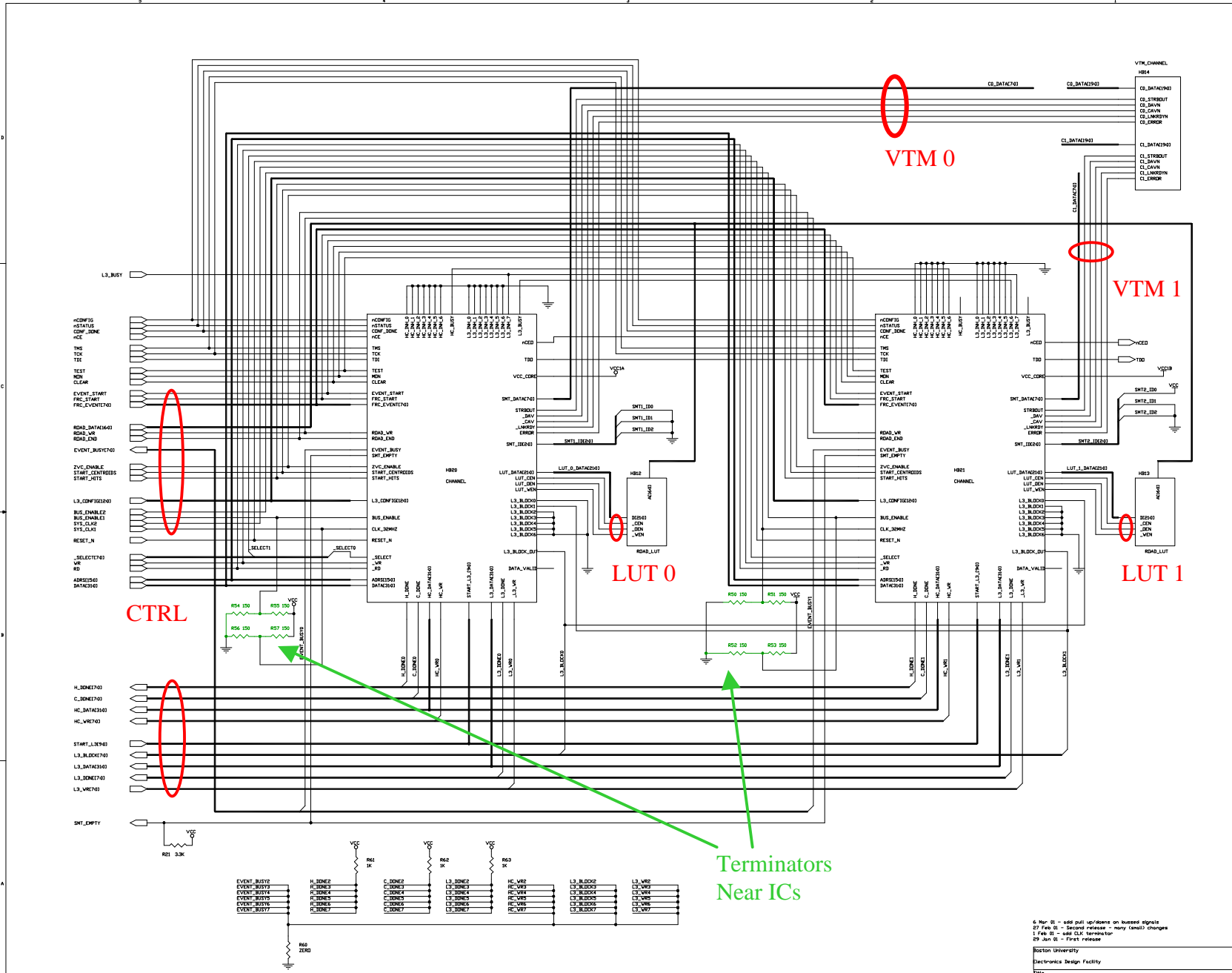
Boston University Electronics Design Facility		
Title		
D0 SILICON TRIGGER CARD		
Size	Document Number	Rev
A	PCI_1_INTERFACE	A
Date:	Monday, March 05, 2001	Sheet 14 of 20

# PCI Bus 2





# Two Channels



6 Mar 01 - add pull up/resistor on busse signals  
 27 Feb 01 - Second release - many small changes  
 1 Feb 01 - add CLK terminator  
 29 Jan 01 - First release  
 Boston University  
 Electronics Design Facility  
 Title