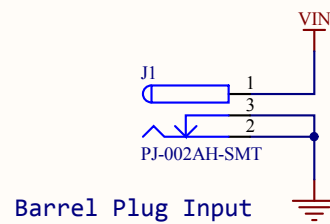
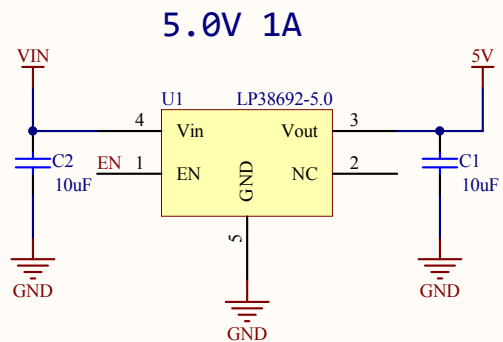
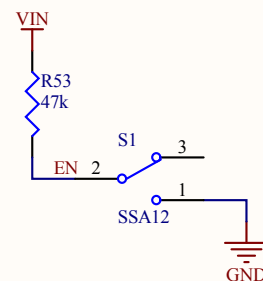
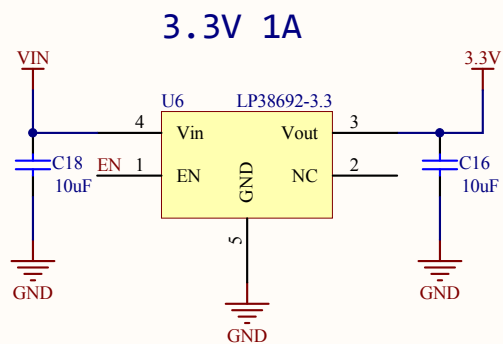
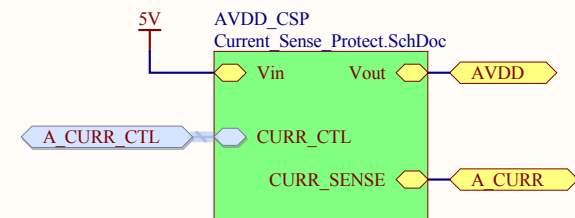


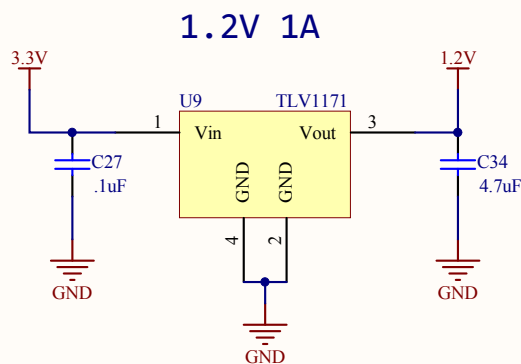
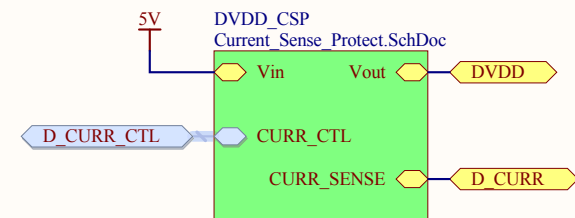
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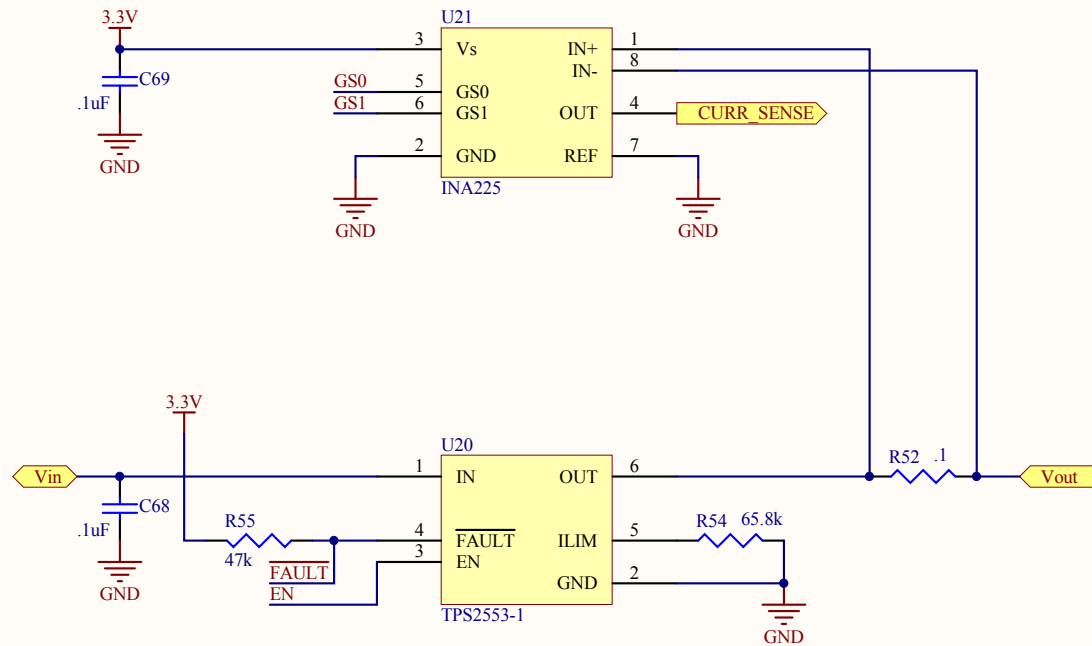
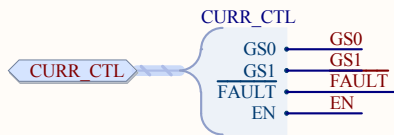
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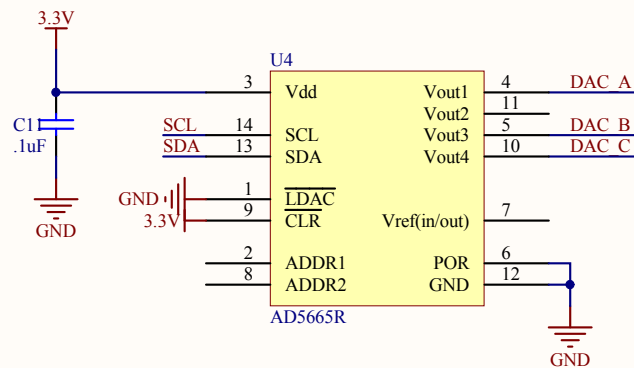
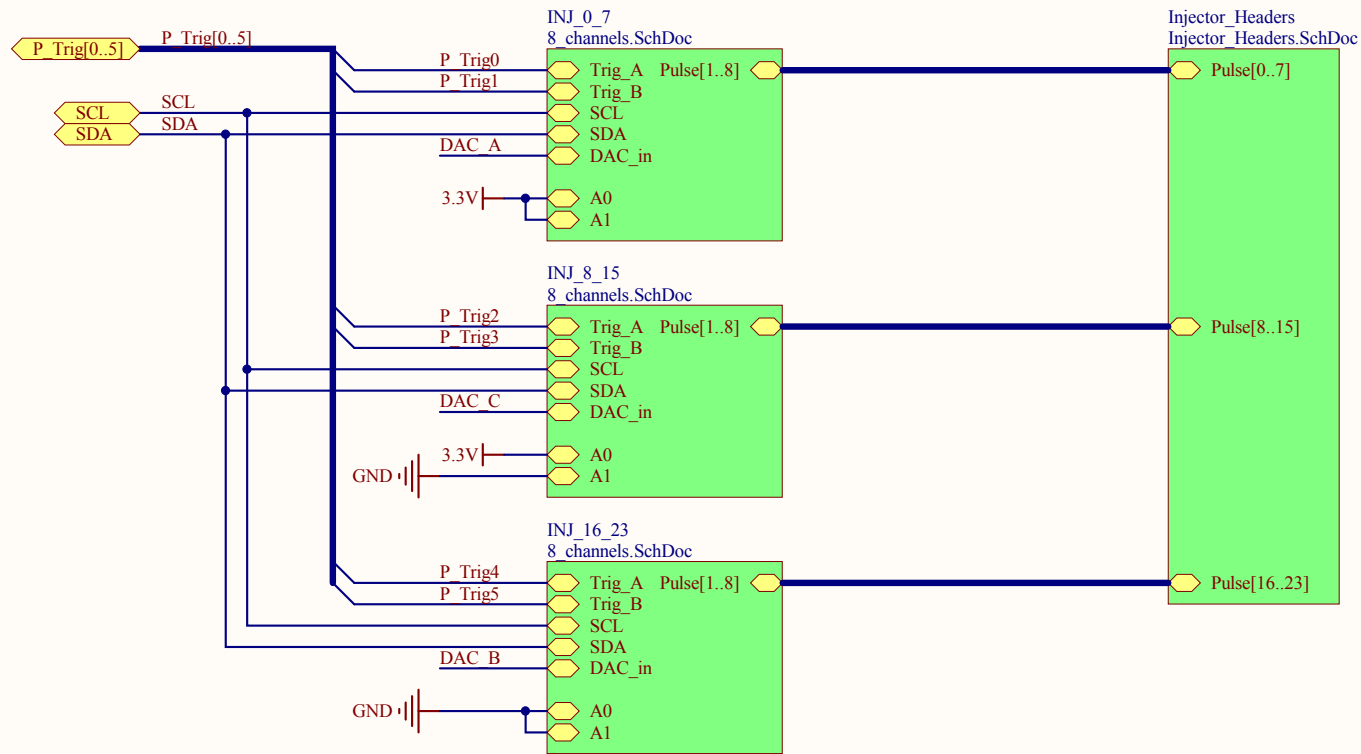
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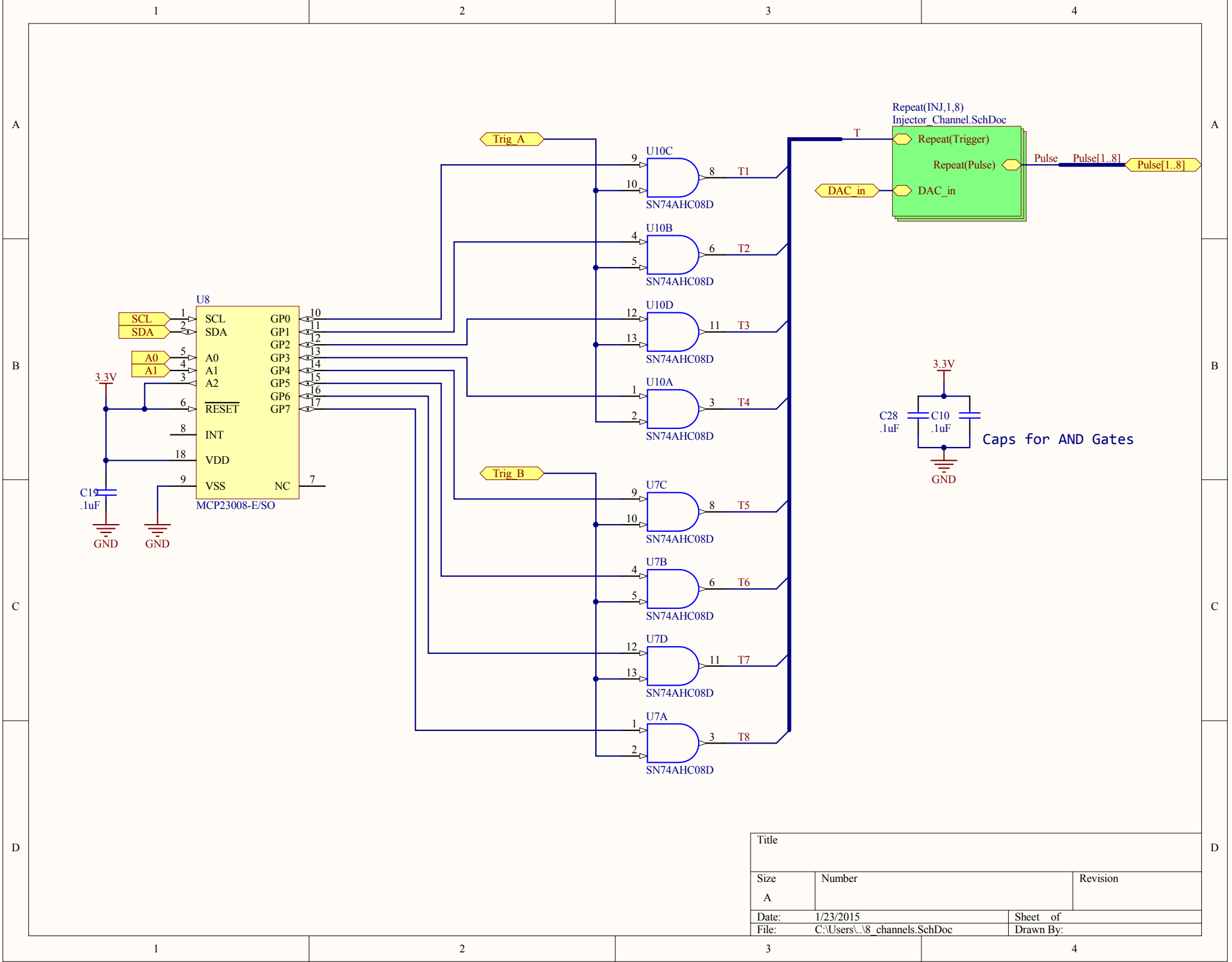
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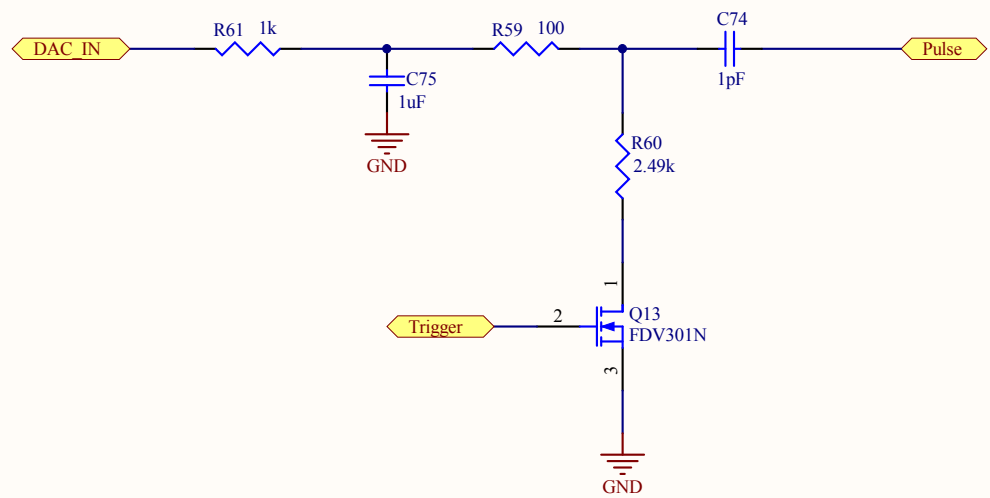
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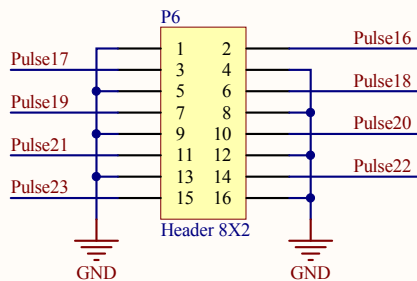
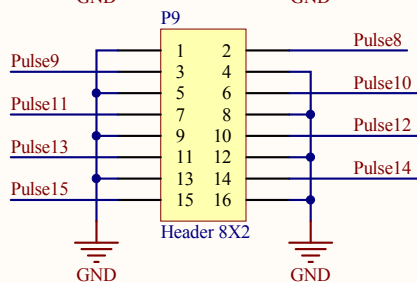
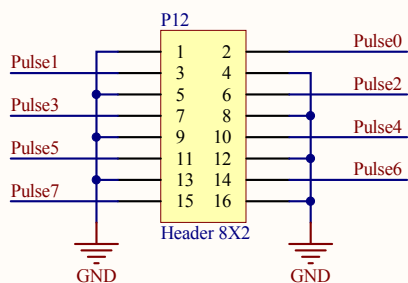
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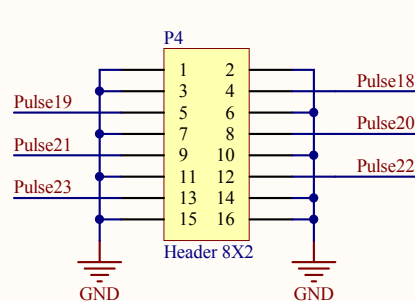
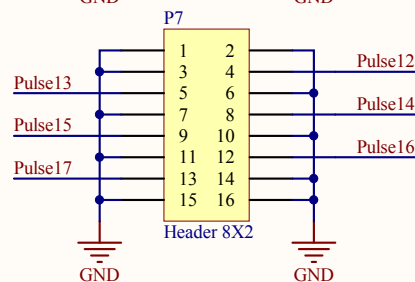
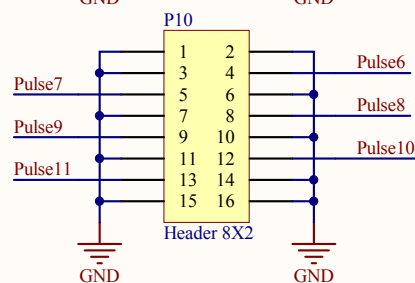
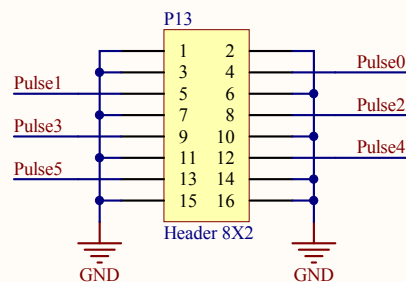
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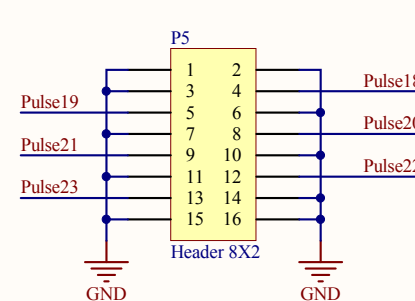
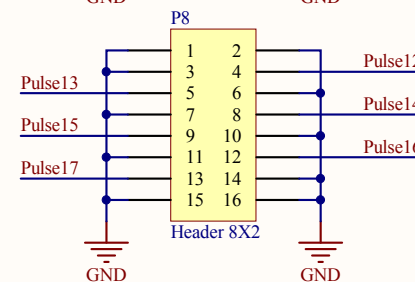
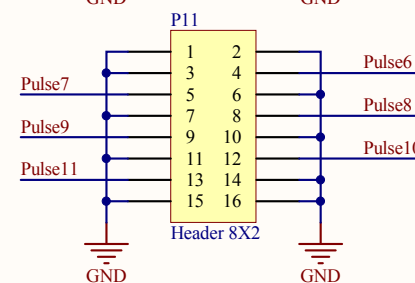
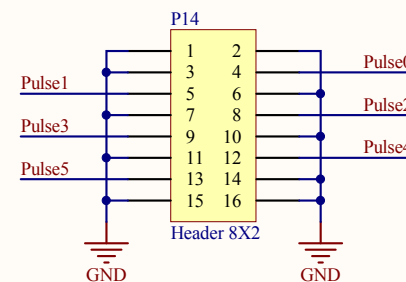
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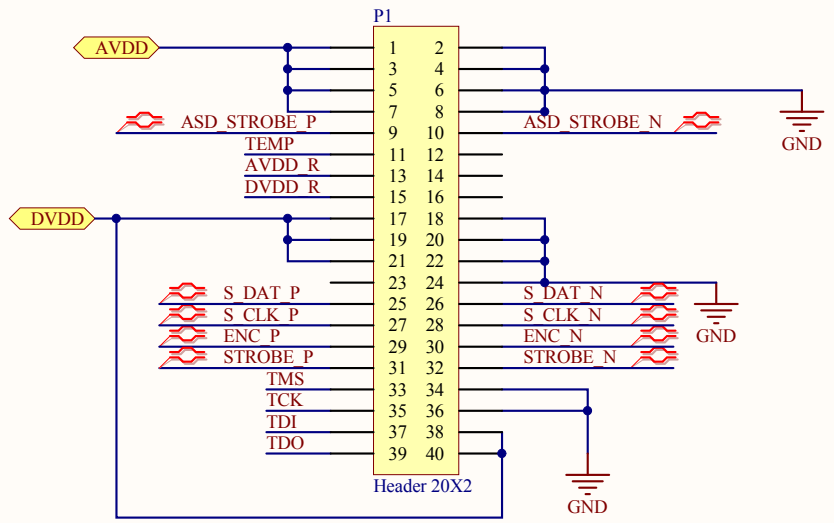
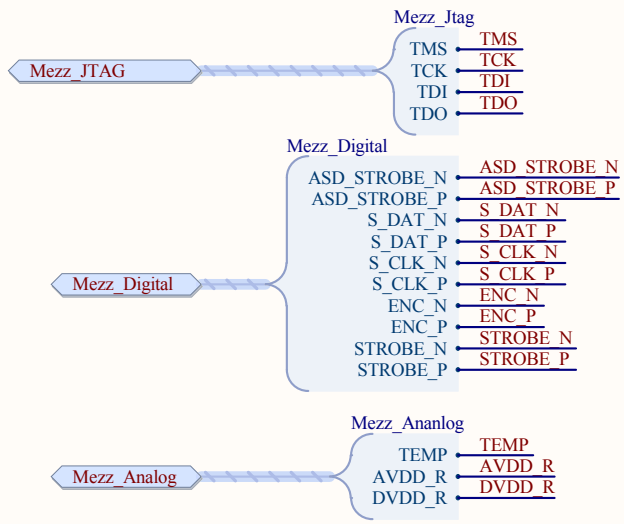
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mdt446



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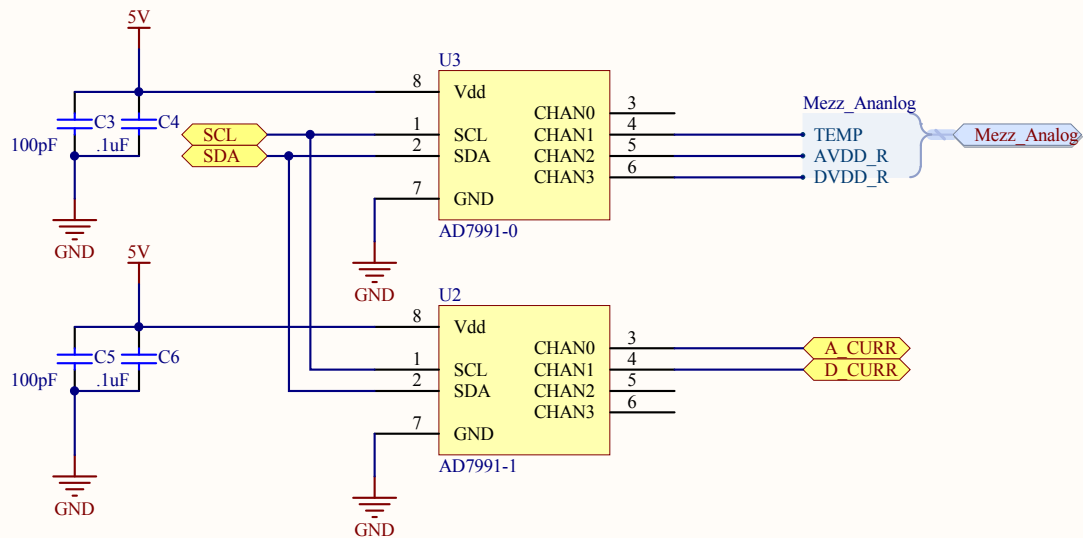
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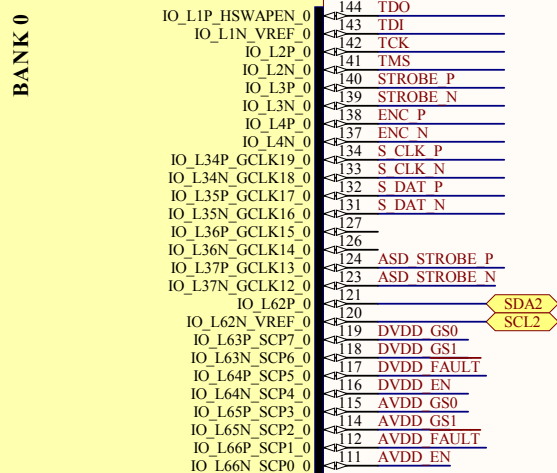
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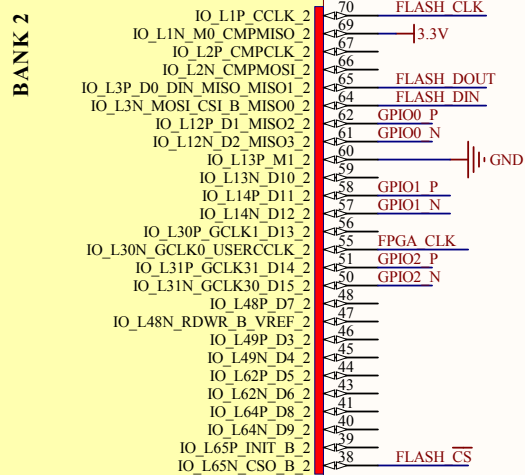
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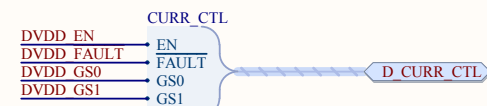
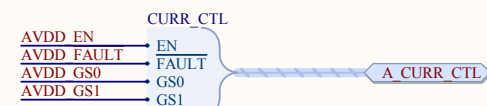
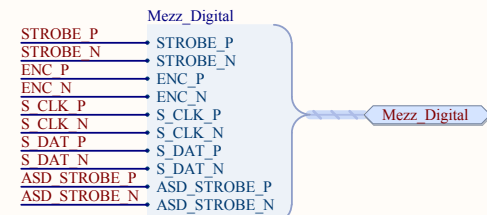
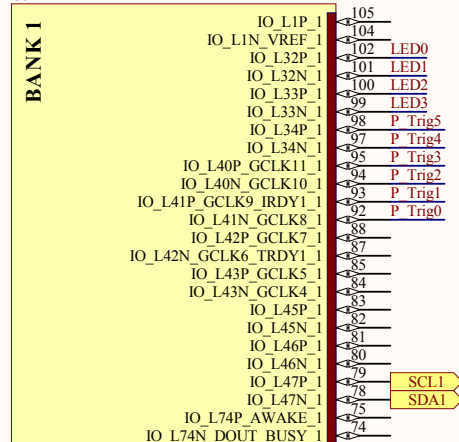


LVDS_33 Output only available on bank 0 & 2

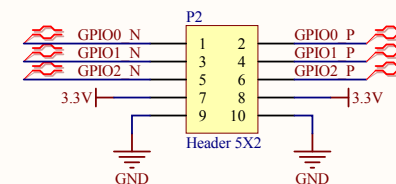
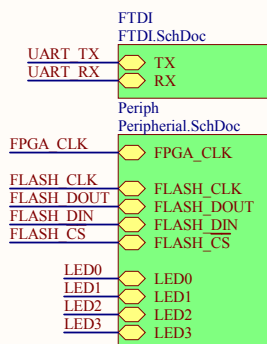
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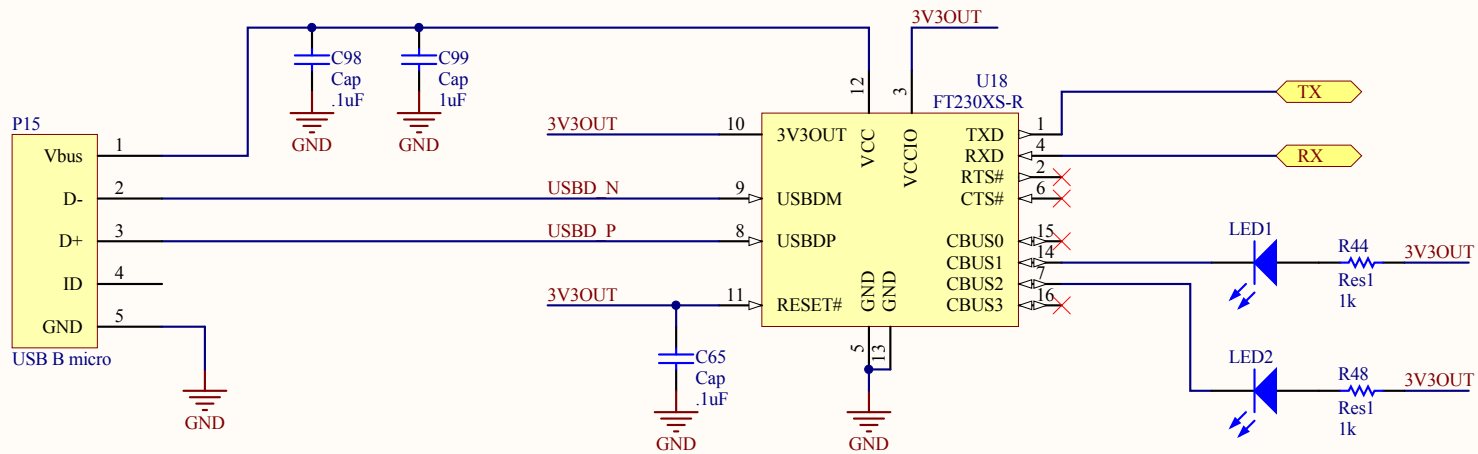
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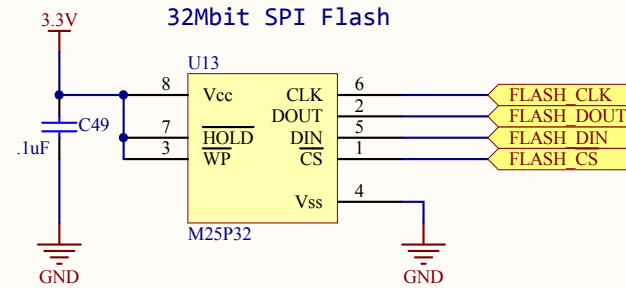
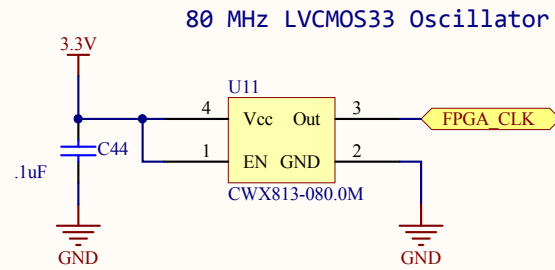
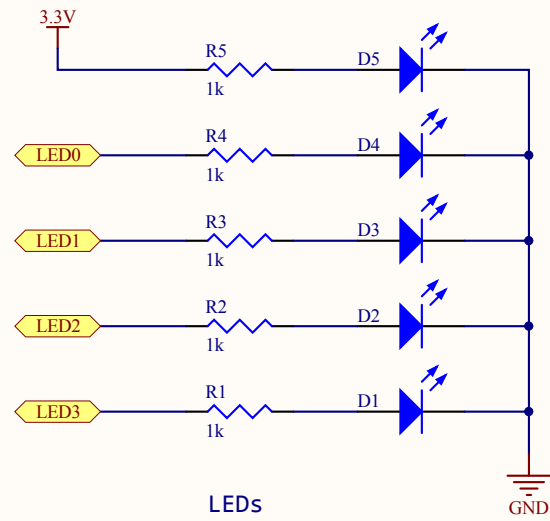
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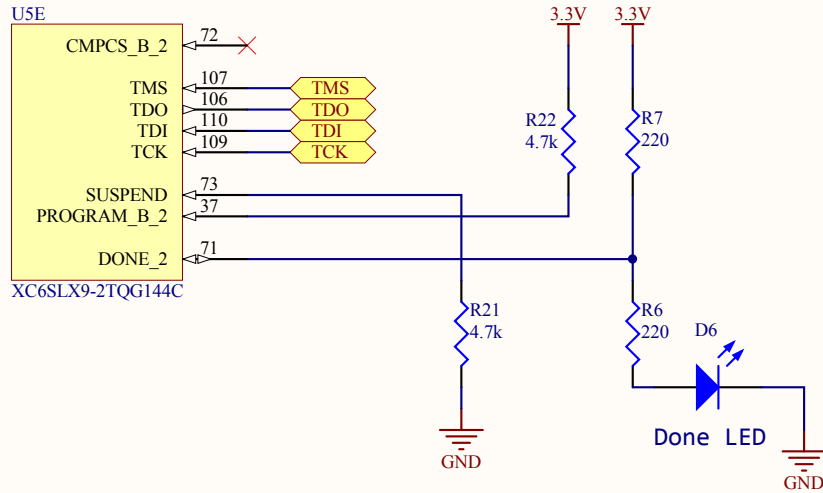
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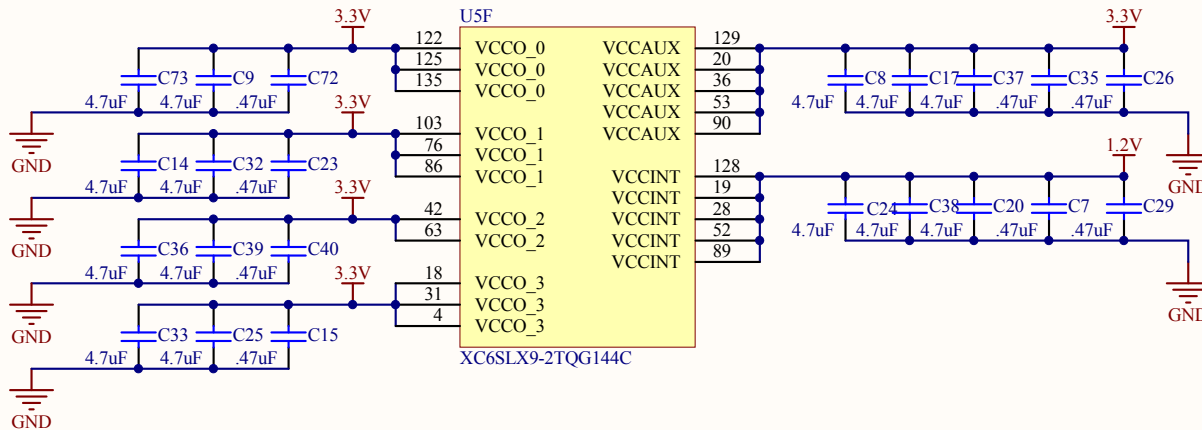
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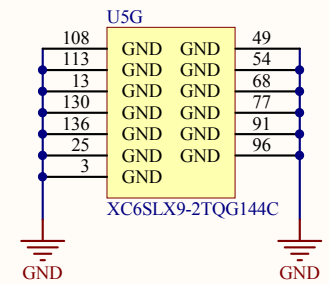
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FPGA Power Banks



FPGA GND



Title		
Size	Number	Revision
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